



Patrick Lampl, MSc. BSc.

Date of birth: 17/11/1987

Nationality: Austrian

Gender: Male

CONTACT

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WORK EXPERIENCE

09/2017 – CURRENT

Lecturer

FH-JOANNEUM GmbH

Bachelor Courses:

- Basic Electronics Lab (Basics about units, circuits and programming)
- Digital Systems (from transistors to gates and Boolean logic to FPGA designs)
- Supervision of bachelor theses

Master Courses:

- Microcontroller Architectures and Programming (Design and assembly programming of a simple general purpose computer, concepts in modern computer architectures and benchmarking their effects)
- Supervision of master projects

typical Research/Project tasks:

- creation and testing of HDL IP cores (ADC/DAC interfaces, signal processing, error monitoring, parametrization)
- creation of FPGA top level designs (system level view)
- adapting models for HDL code generation
- consulting for Simulink control engineers in regards to FPGA usage.

Education / www.fh-joanneum.at / Alte Poststraße 149, 8020, Kapfenberg, Austria

09/2014 – 08/2017

Researcher

FH-JOANNEUM GmbH

see entry for Lecturer

Kapfenberg, Austria

2013 – 08/2014

Student employee

FH-JOANNEUM GmbH

Planning of the lecture schedule once for all three bachelor cohorts and once for all five bachelor and master cohorts.

Market research in the area of power electronics.

Firmware programming and RF-PCB design of an FMCW radar for snow stratigraphy.

Kapfenberg, Austria

EDUCATION AND TRAINING

10/2011 – 04/2014 – Kapfenberg, Austria

Master of Science (MSc.)

University of Applied Sciences FH Joanneum

Field(s) of study

- Advanced Electronic Engineering

10/2008 – 10/2011 – Kapfenberg, Austria

Bachelor of Science (BSc.)

University of Applied Science FH Joanneum

Field(s) of study

- Elektronik & Technologie Management

Excellent | Simulation of Radar System for Metal Detection, Detection of Metallic Bulk Material | EQF level 6 | ECTS | 180

OTHER SKILLS

Other skills

- amateur astronomy club member: presentation of an introduction to astronomy for pupils and adults.